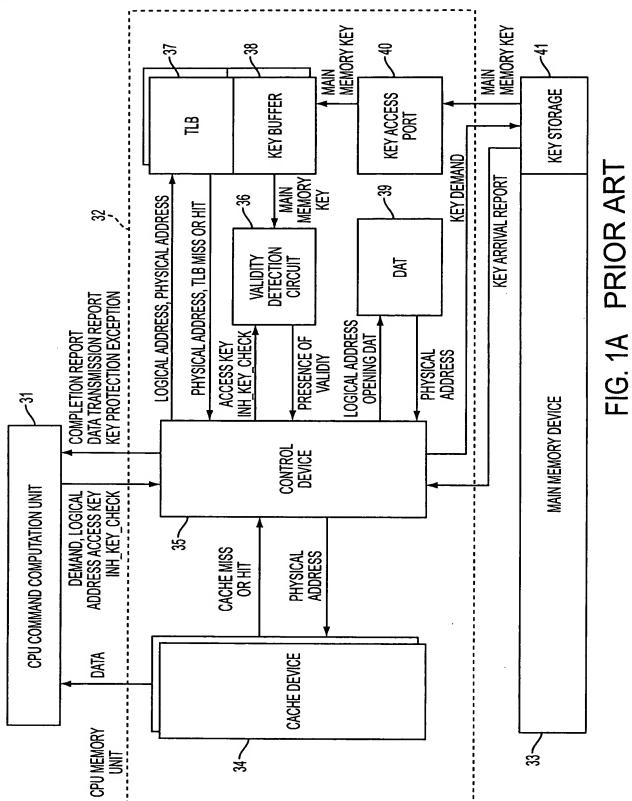


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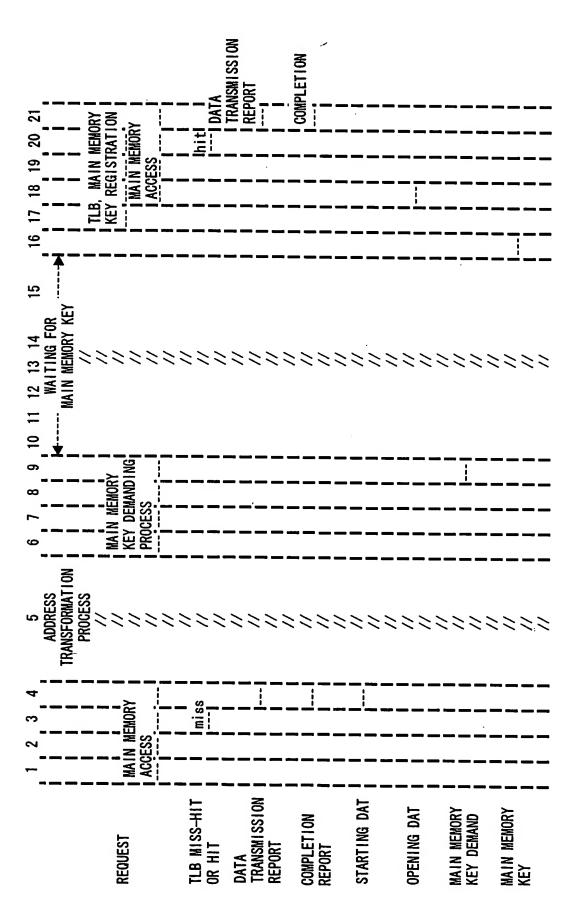
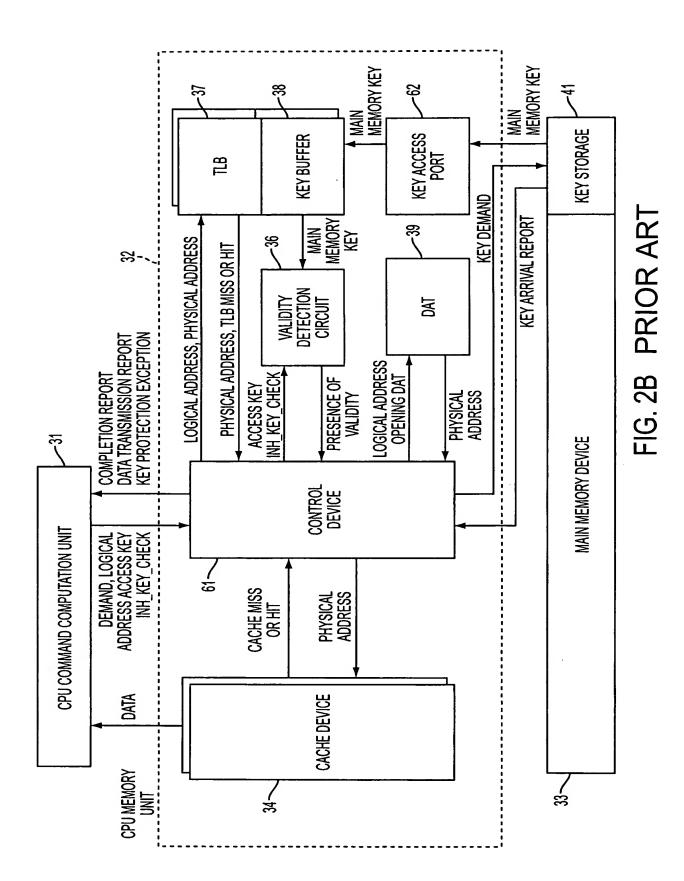


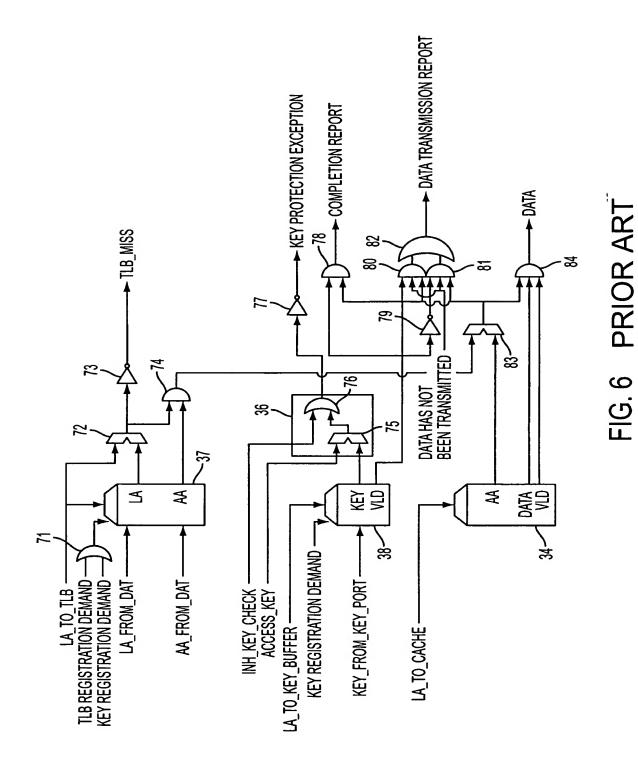
FIG. 1B PRIOR ART

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START S11 Υ TLB HIT? **S17** N **S12 CACHE HIT?** TRANSFORMATION OF **S18** ADDRESS AT DAT N **S13 DEMANDING DATA TO BE REGISTERED** MAIN MEMORY KEY **DEMAND S19** N **S14** IS KEY CHECK REQUIRED? HAS MAIN N MEMORY KEY ARRIVED? Υ S20 **S15** N IS ACCESS **REGISTRATION** VALID? **S16 EXCEPTION REPORT OPENING DAT** DATA **S22** TRANSMISSION, ·S21 **COMPLETION REPORT END**

FIG. 1C PRIOR ART





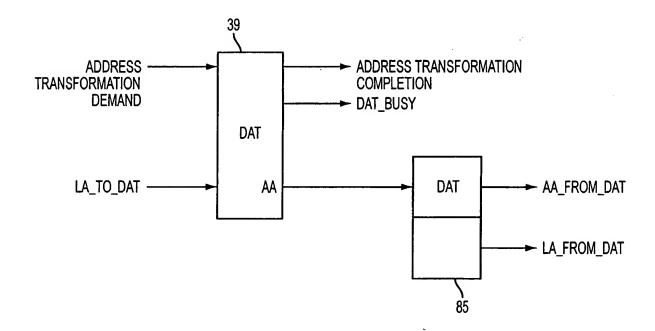


FIG. 7 **PRIOR ART**

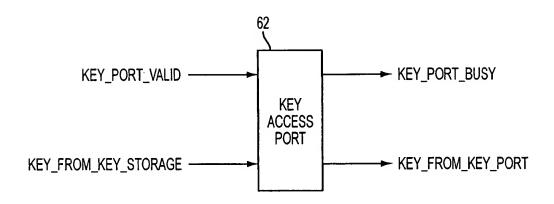


FIG. 8 **PRIOR ART**

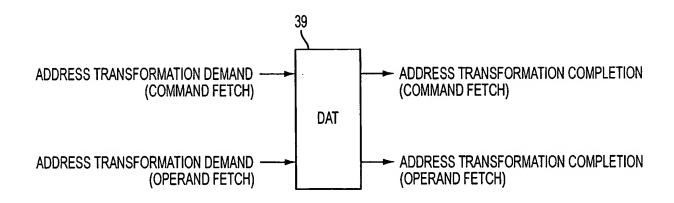


FIG. 10 **PRIOR ART**

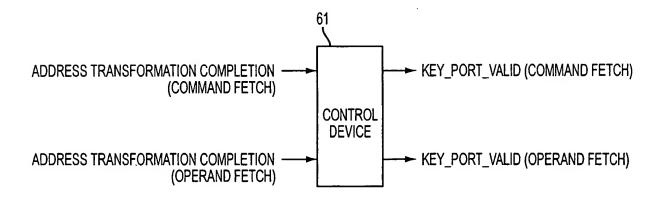


FIG. 11 **PRIOR ART**